fairlight ENGINEERING CHANGE NOTE: mfx3

ECN #:   392

ASSEMBLY:	BMW4061	**DESCRIPTION:**	MW4-AES1 Digital Input/Output
New Assembly:		**DATE:**	25/10/00
Current Assy Rev:	C	**Current Schematic Rev:**	1.2 2.1
New Assy Rev:	D	**New Schematic Rev:**	1.3 2.2

○ CMS BOMs Updated   ◉ **Excel BOMs Updated**   ◉ **Subcontractor Records Updated**   **Compatibility Maintained:** Yes

IMPLEMENTATION INFORMATION:

○ **Safety Issue**   ○ **UL Compliance Issue**   ○ **New Feature**   ○ **Cost Reduction**

◉ **Bug Fix**   ○ **EMC Compliance Issue**   ○ **Quality Issue**

○ **Reliability Reasons**   ○ **Software Change**   ○ **Cosmetic Change**

REASON FOR CHANGE:

To fix noise on AES inputs

Note: This change is mandatory no matter it is a 74FCT244 or 74LVC244 loaded at U16. And should also apply to both Rev A & Rev B PCBs

DETAILS OF CHANGE:

Stop doing changes described in ECN385. U16 will change back to 74FCT244 in the BOM. If ECN385 has been done, do not need to change back. But still should have this ECN applied.

Lift U16/5 and U16/7

Connect RP4/2 to U14/33 (there is a via near RP4/2)

Connect RP4/3 to U14/39.

For Rev A PCB: Put solder blobs across J3 and J4 to raise the R&D Revision Code to 1.3.

For Rev B PCB: Put a solder blob across J3 and remove solder blob across J4 to raise the R&D Revision Code to 2.2.

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If it is a 74FCT244 loaded at U16, place a assembly revision label on the card, marked "D", after the blank PCB P/N so it reads as " BMW4061A-D" (For Rev A PCB) and " BMW4061B-D" (For Rev B PCB)

If it is a 74LVC244 loaded at U16, place a assembly revision label on the card, marked "1D", after the blank PCB P/N so it reads as " BMW4061A-1D" (For Rev A PCB) and " BMW4061B-1D" (For Rev B PCB)

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HW or Project Engineer…………………………………………………………………………

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R&D or Project Manager……………………………………………………………………………